## FORM PTO-1449 (MODIFIED)

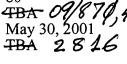
## LIST OF PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT

Thaddeus John Gabara Applicant(s):

Case:

Serial No.: Filing Date: Group:

May 30, 2001





| U.S. PATENT DOCUMENTS    |              |          |                                 |                   |                |  |
|--------------------------|--------------|----------|---------------------------------|-------------------|----------------|--|
| EXAMINER                 |              |          |                                 |                   | FILING DATE    |  |
| INITIAL                  | DOCUMENT NO. | DATE     | NAME                            | CLASS/SUBCLASS    | IF APPROPRIATE |  |
| HLN 1.                   | 09/162,852   | 9/29/98  | T.J. Gabara and S.A. Mujtaba, " | Multi-Input Compa | rator."        |  |
| HN 2.                    | 6,191,623    | 02/20/01 | Gabara                          | 327/71            | 09/29/98       |  |
| H2N3.                    | 6,097,768    | 08/01/00 | Janesch et al.                  | 375/80            | 1412/97        |  |
| #W 4.                    | 5,877,718    | 03/02/99 | Andoh et al.                    | 341/155           | 03/24/97       |  |
| FLN 5.                   | 5,790,063    | 08/04/98 | Koifman et al.                  | 341/155           | 12/16/96       |  |
| fen 6.                   | 5,636,252    | 06/03/97 | Patel et al.                    | 375/345           | 12/15/95       |  |
| # 7.                     | 5,606,625    | 02/25/97 | Dallavalle et al.               | 384/107           | 05/26/94       |  |
| <b>#</b> 8.              | 5,576,837    | 11/19/96 | Strolle et al.                  | 386/33            | 08/26/94       |  |
| HIN 9.                   | 5,563,531    | 10/08/96 | Meyer                           | 54//5             | 09/15/94       |  |
| 10                       | . 4,767,949  | 08/30/88 | Mayweather, III                 | 327/80            | 05/01/87       |  |
| FOREIGN PATENT DOCUMENTS |              |          |                                 |                   |                |  |
| EXAMINER                 |              |          |                                 |                   | TRANSLATION    |  |
| INITIAL                  | DOCUMENT NO. | DATE     | COUNTRY                         | CLASS/SUBCLASS    | YES NO         |  |

| OTHED | DOCUMENTS |
|-------|-----------|

EXAMINER

AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC

1. J. Montanaro et al., "A 160-MHz, 32-b, 0.5-W CMOS RISC Microprocessor," IEEE Journal of Solid-State Circuits, Vol. 31, No. 11, pp. 1703-1714, 1996.

2. J.-C. Kim et al., "CMOS Sense Amplifier-Based Flip-Flop with Two N-C2MOS Output Latches," Electronics Letters, Vol. 36, No. 6, pp. 498-500, 2000.

3. "MOS Operational Amplifiers," Chapter Four, pp. 121-126, undated.

\* Note: Not considered because undated

Examiner HAI L. NGUYEN **Date Considered** 

Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.